

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Tyson R. McGuffin, et al.

Confirmation No.:

Application No.:

Examiner:

Filing Date:

Group Art Unit:

Title: AREA BASED POWER ESTIMATION

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)

under 37 CFR 1.97(c) together with either a:
 Statement under 37 CFR 1.97(e), or
 a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)

under 37 CFR 1.97 (d) together with a:
 Statement under 37 CFR 1.97(e)(1) or (2), and
 a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EU853429734US**

Respectfully submitted,

Date of Deposit **August 6, 2003**

Tyson R. McGuffin, et al.

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By Lisa D. Jones
Typed Name: Lisa D. Jones

By Christopher P. Harris

Christopher P. Harris

Attorney/Agent for Applicant(s)
Reg. No. **43,660**

Date: **August 6, 2003**

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200208596-1		
	APPLICANT Tyson R. McGuffin		
	FILING DATE	GROUP	

REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
1A	5,880,967	03/09/1999	Jyu, et al.	
1B	5,949,723	09/07/1999	Clemen, et al.	
1C	6,097,207	08/01/2000	Bernstein, et al.	
1D	6,209,122 B1	03/27/2001	Jyu, et al.	
1E	6,339,835 B1	01/15/2002	Reddy, et al.	
1F	6,513,145 B1	01/28/2003	Venkitakrishnan	
1G	6,518,796 B1	02/11/2003	Stan, et al.	
1H				
1I				
1J				
1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
		1L				
		1M				
		1N				
		1O				
		1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Paul Wiley, et al., "Building Robust, Reliable Nanometer ICs", EEdesign An EE Times Community, July 1998, pp. 1-8.
1R	Synopsys Products & Solutions, "PathMill: Transistor-Level Static Timing Analysis", www.synopsys.com/products/analysis/pathmill_ds.html , March 8, 2000, pp. 1-3.
1S	Synopsys PathMill--Datasheet, "PathMill DataSheet; Critical Path and Static timing Analysis", idec.kaist.ac.kr/CAD/epic/pathmill.html , Synopsys, Inc. October 13, 1997, pp. 1-4.

EXAMINER

DATE CONSIDERED

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200310257-1		
	APPLICANT Thomas W. Chen		
	FILING DATE	GROUP	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
1A				
1B				
1C				
1D				
1E				
1F				
1G				
1H				
1I				
1J				
1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Synopsis Products & Solutions, "AMPS Datasheet Intelligent Design Optimization", www.synopsys.com/products/analysis/amps_ds.html , Synopsys, Inc. November 29, 1999, pp. 1-3
1R	Tanay Karnik, et al., "Total Power Optimization By Simultaneous Dual-Vt Allocation and Device Sizing in High Performance Microprocessors", ACM, June 2002
1S	Michel Crote, Philippe Hurat, " Better Cells for Better Designs Power and Performance Optimization of Cell-Based Designs with Transistor-level Cell Sizing, Chapter 9, pp. 1-16

EXAMINER

DATE CONSIDERED